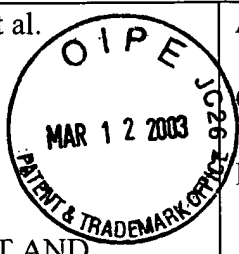


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

# 7/A  
T. BELL  
3-20-03

Applicant(s): HUTAMURA et al.	Atty. Dkt.: 11-058
Serial No.: 09/944,118	Group Art Unit: 2836
Filed: September 4, 2001	Examiner: NGUYEN, DANNY
Title: REDUCED CURRENT AND POWER CONSUMPTION STRUCTURE OF DRIVE CIRCUIT	
Assistant Commissioner for Patents Washington, D.C. 20231	Date: March 12, 2003



CERTIFICATE OF HAND DELIVERY	
I hereby certify that this correspondence is being hand delivered to and deposited with the USPTO at the Customer Service Window, Office of Initial Patent Examination, Crystal Plaza Building 2, Room 1B03, 2011 South Clark Place, Arlington, VA 22202 on March 12, 2003.	
Typed Name: DAVID G. POSZ	
Signature: <u>[Handwritten Signature]</u>	

AMENDMENT

Sir:

In response to the Office Action mailed November 12, 2002, please enter the following amendments and consider the appended remarks.

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IN THE SPECIFICATION

Please replace the paragraph beginning on page 2, line 8 as follows.

Specifically, the drive circuit 1 is a push-pull circuit which is responsive to the control signal *Sa* of a low level inputted to the input terminal 2 to turn on the transistor *T1* and off the transistors *T2* and *T3* so that the voltage *Vb* is applied from the power supply line 5 to the gate of the MOSFET 4 to turn on the MOSFET 4. When the control signal *Sa* of a high level is inputted to the input terminal 2, the transistors *T2* and *T3* are turned on, while the transistor *T1* is turned

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